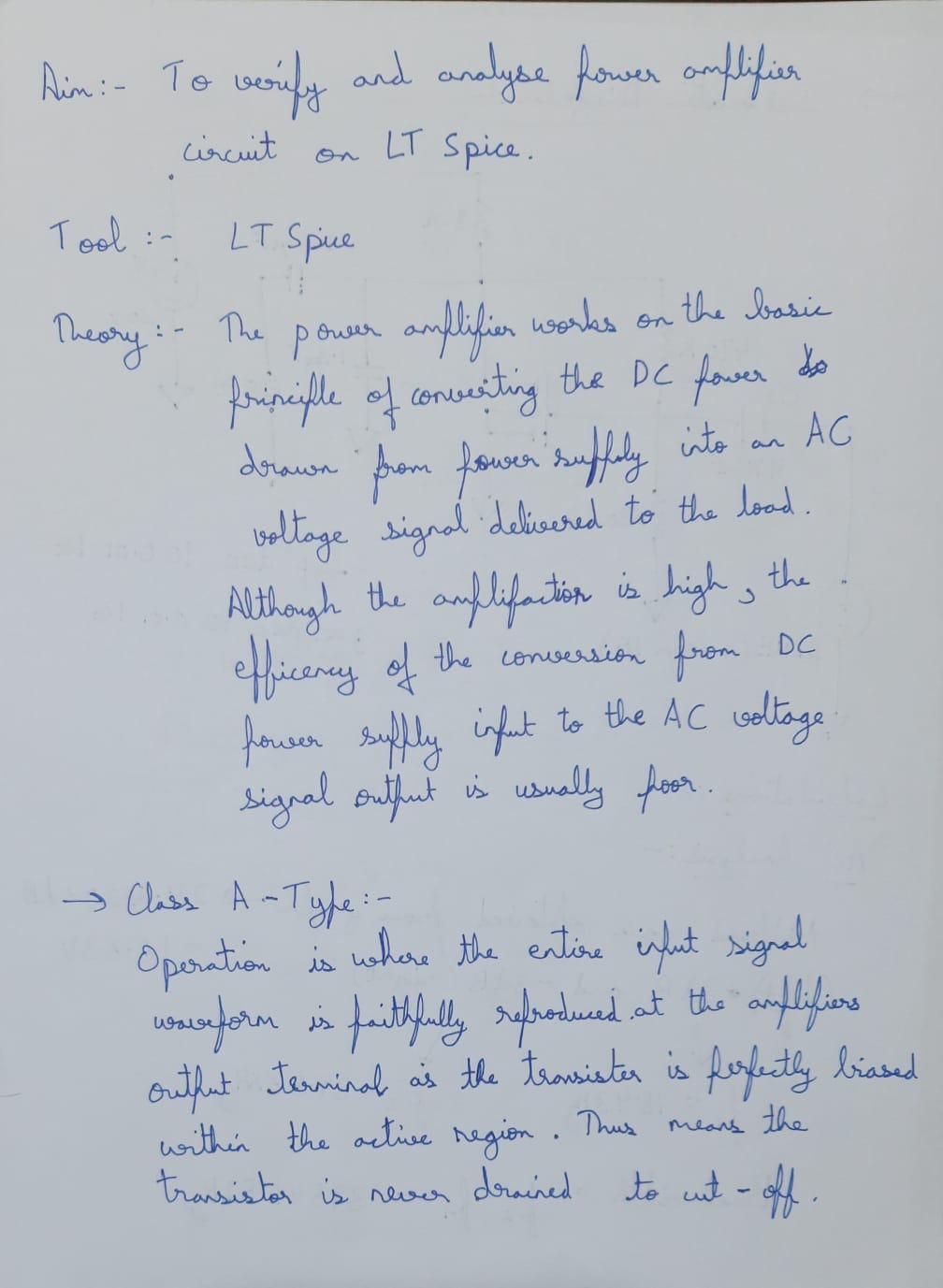
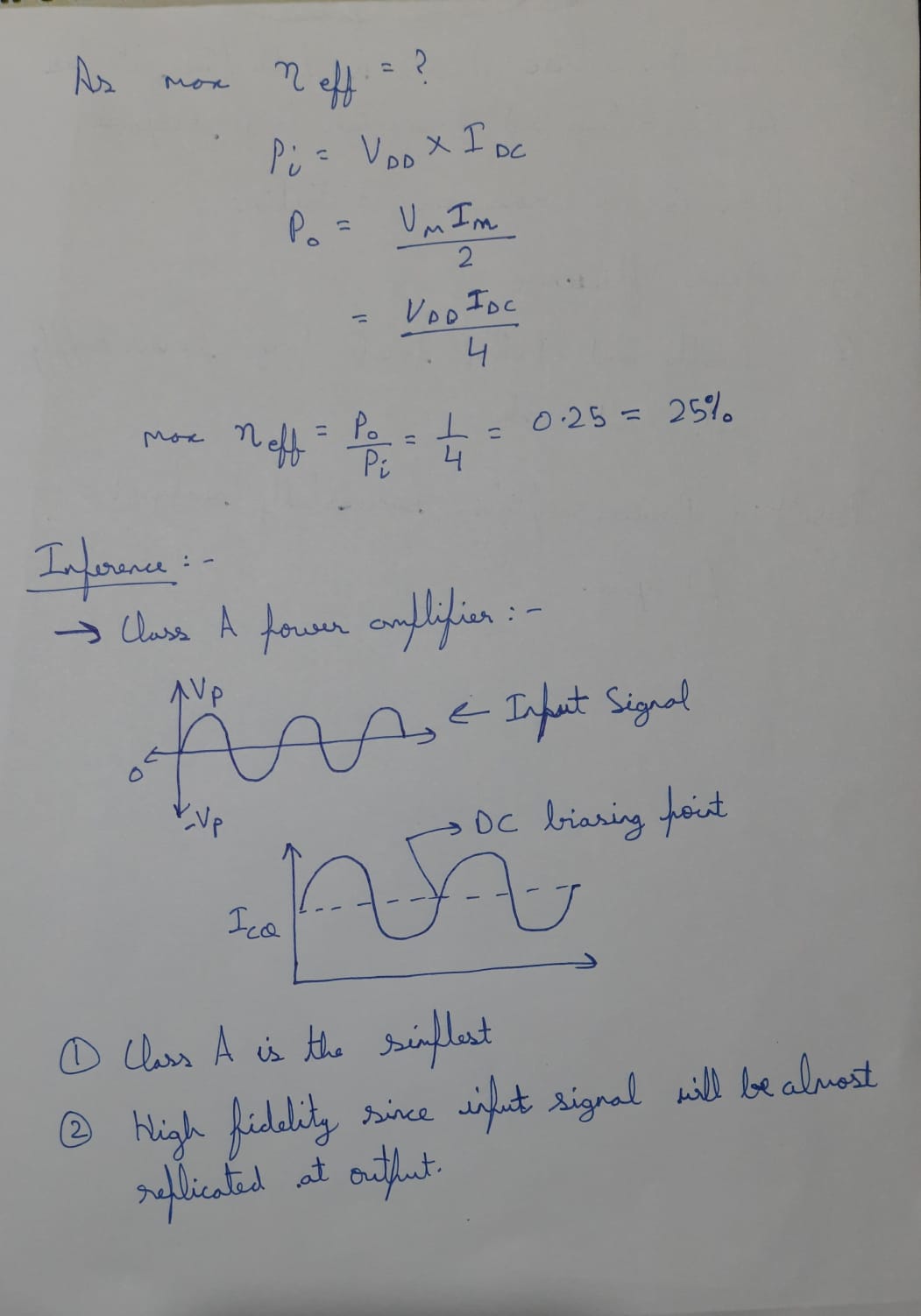
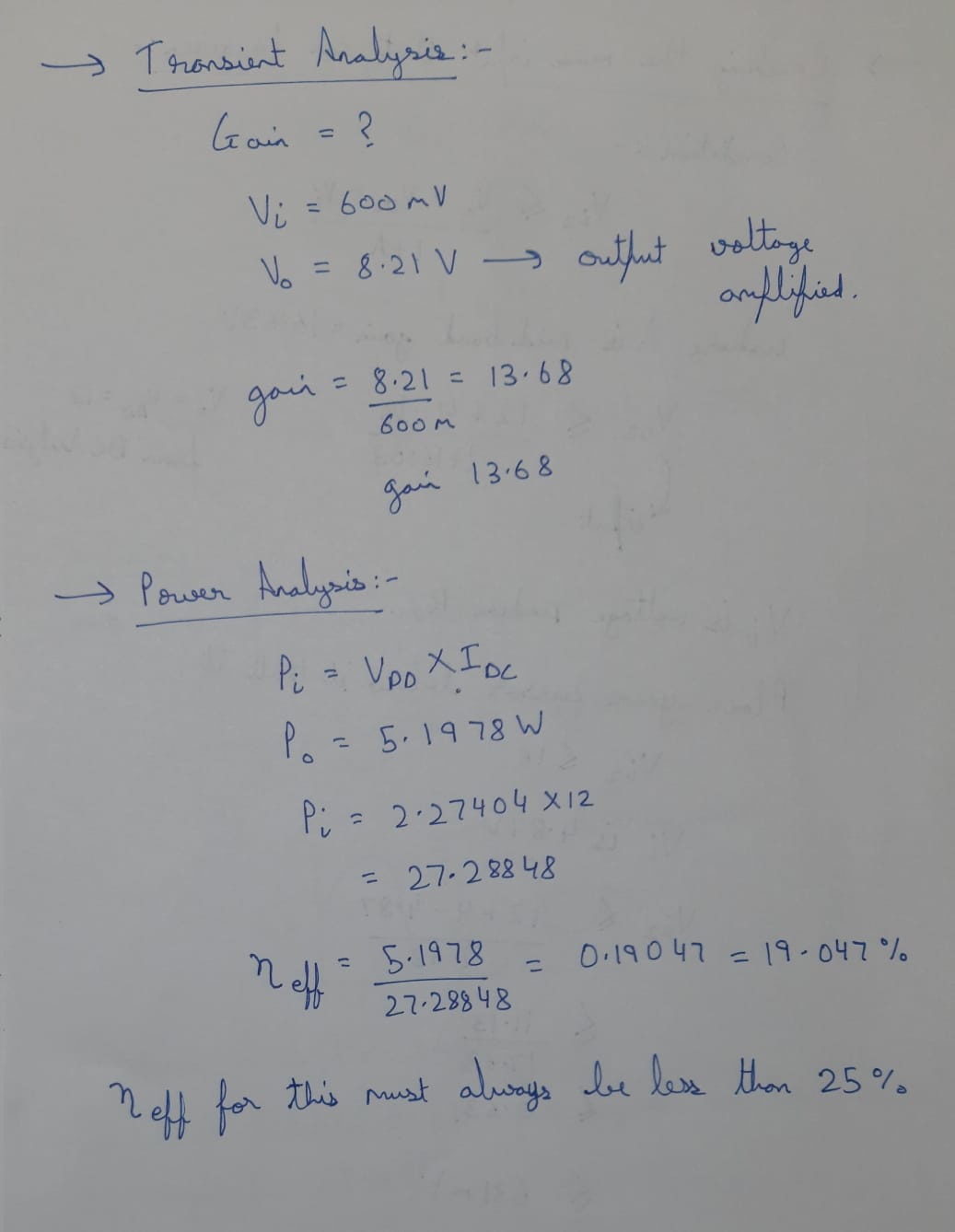
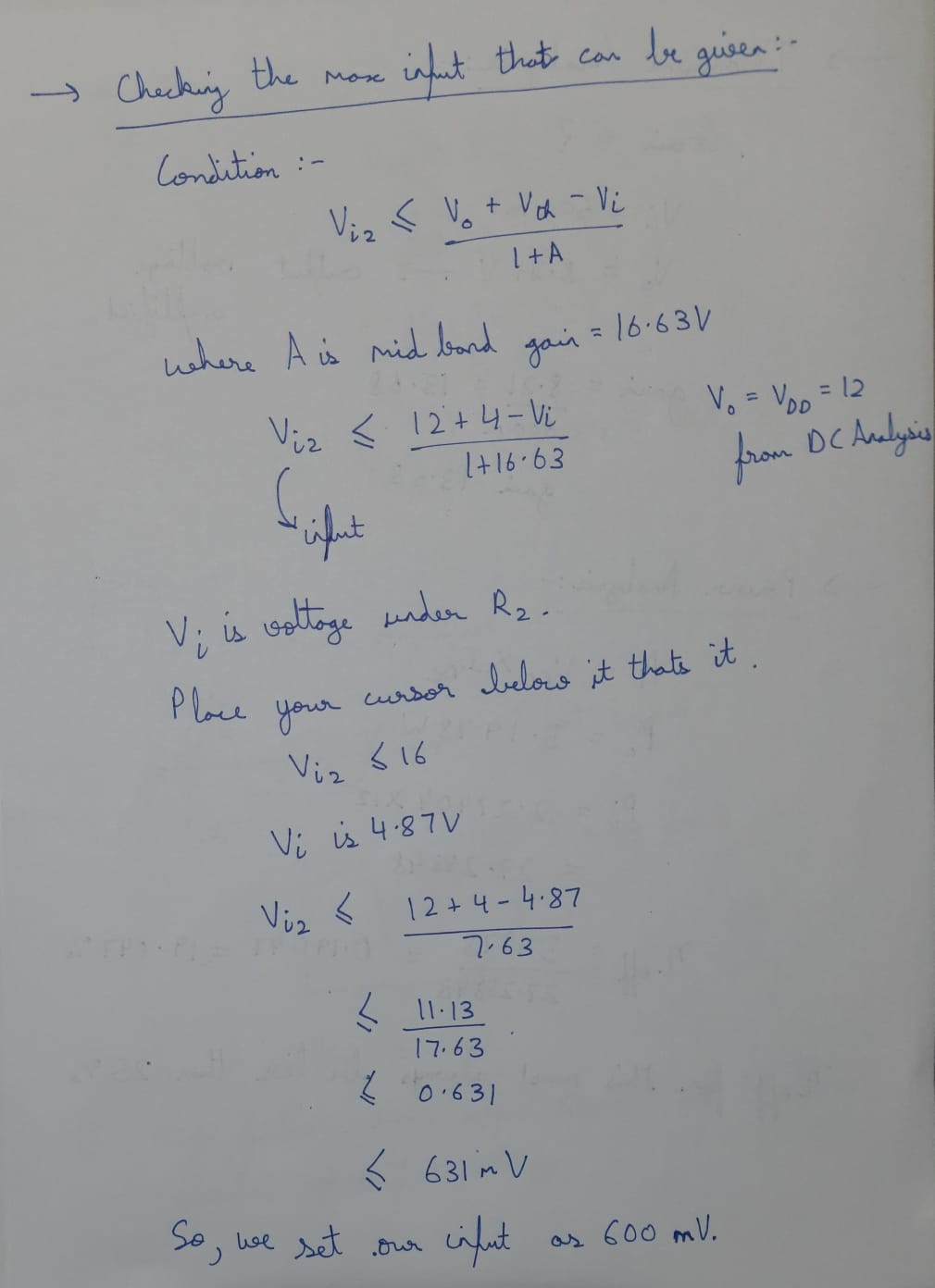
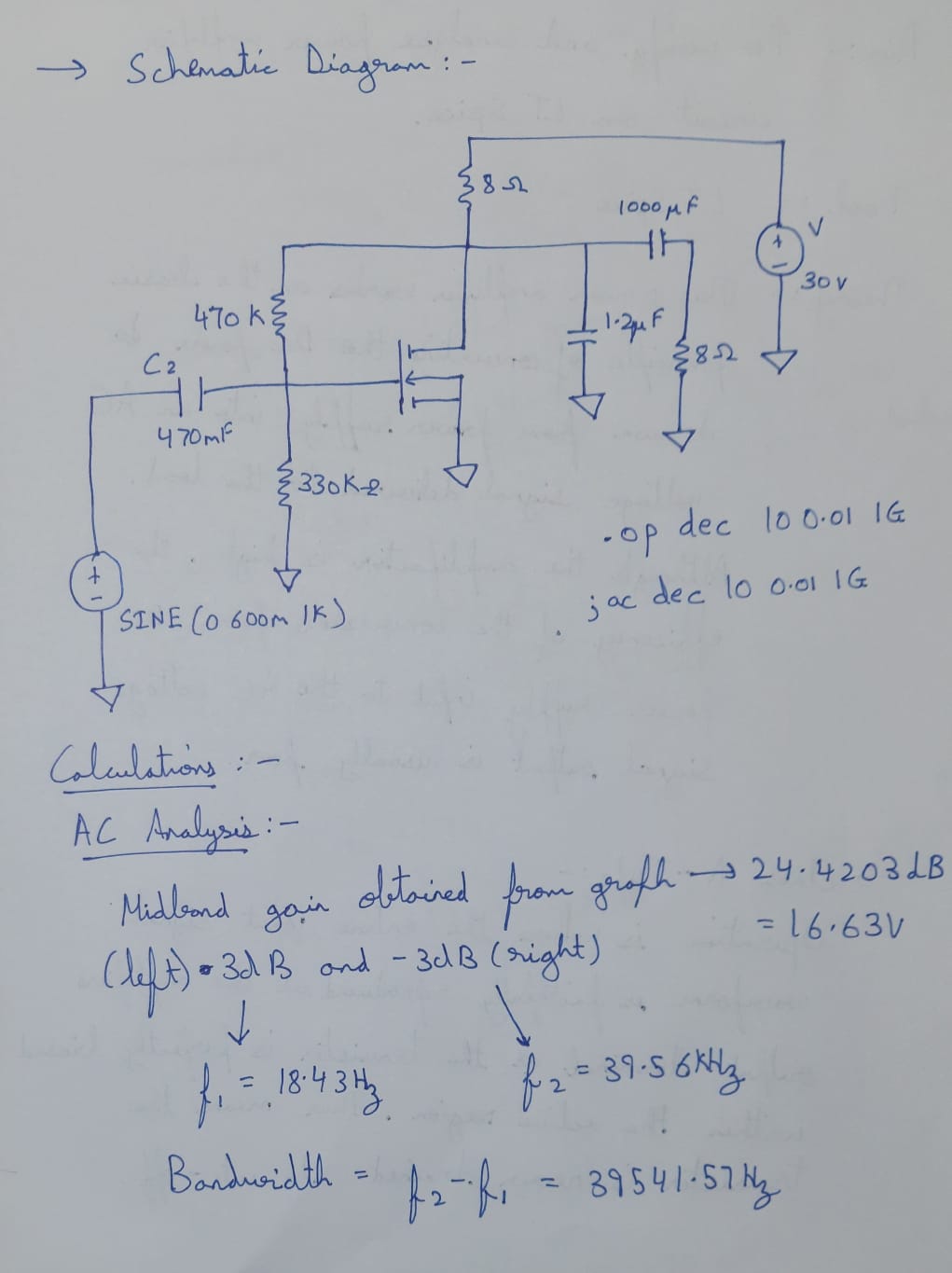
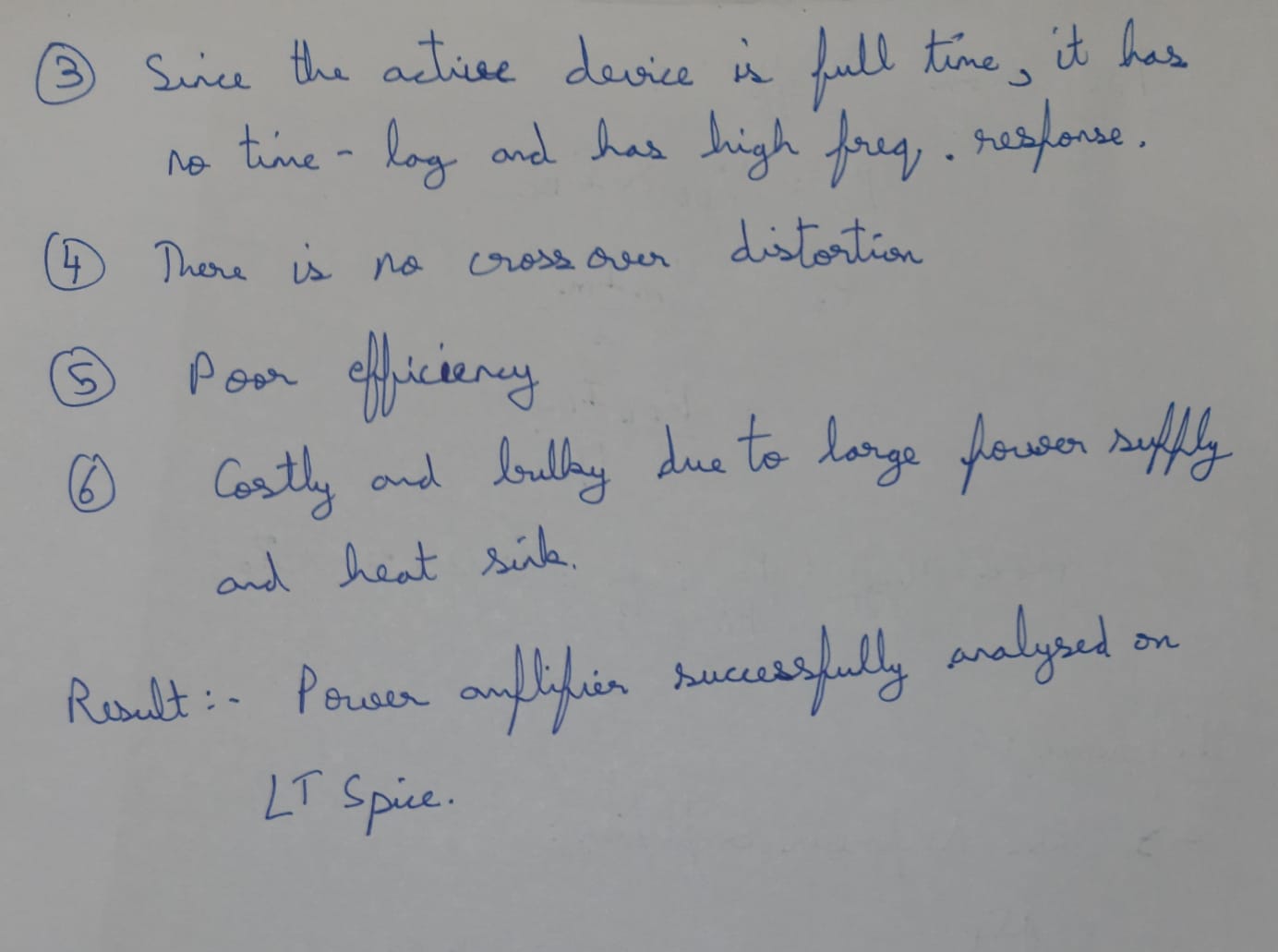
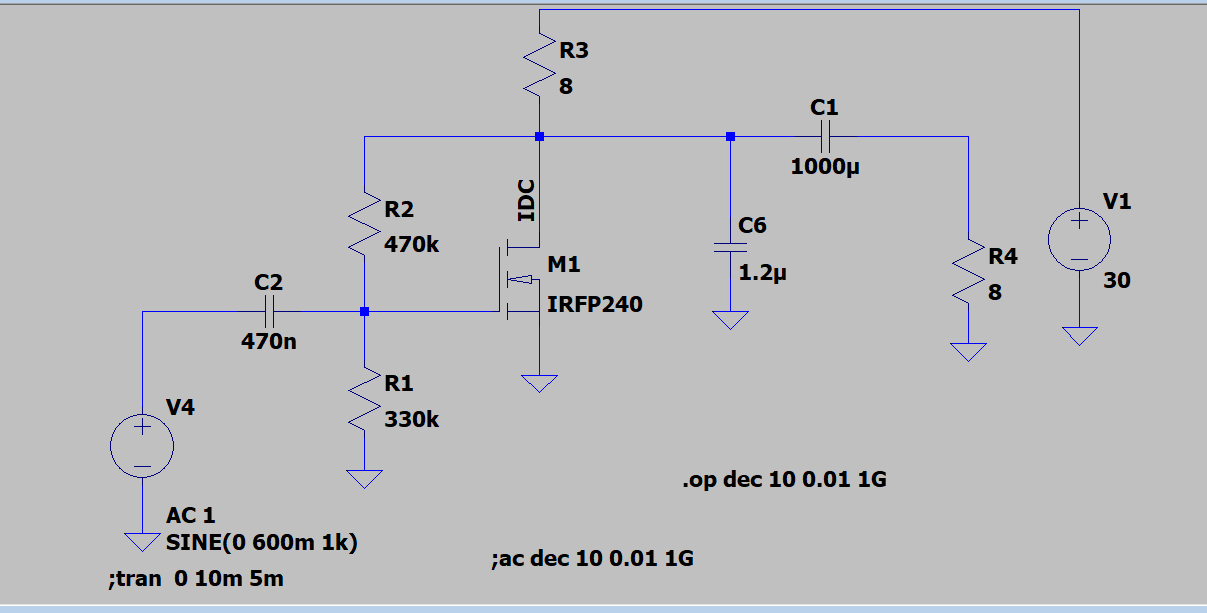
EXPERIMENT – 5







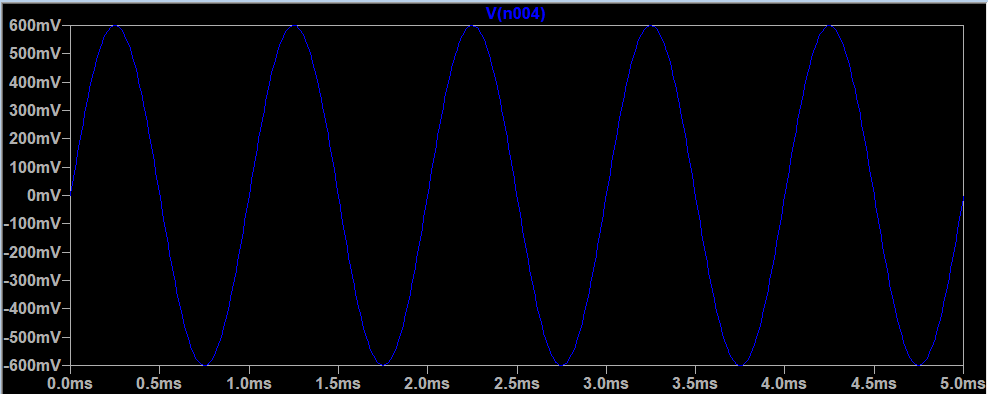
CIRCUIT:



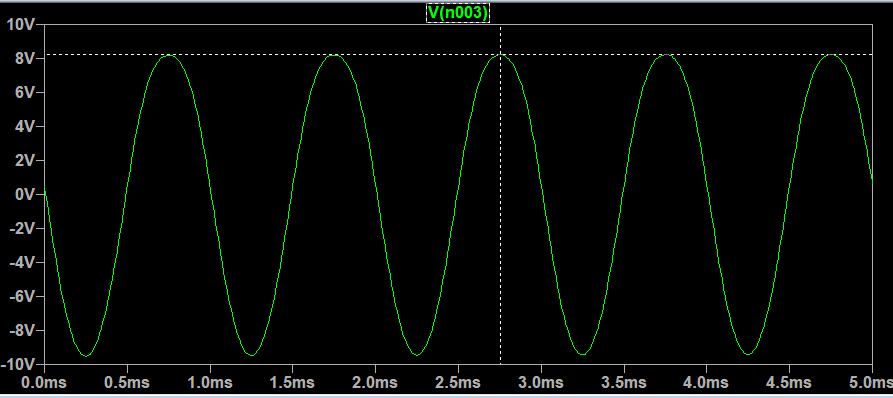
OUTPUT:

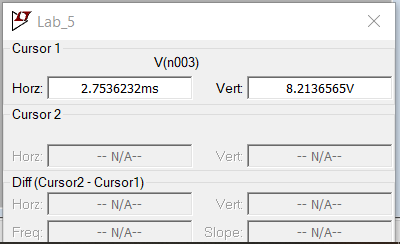
TRANSIENT ANALYSIS:

INPUT SIGNAL:



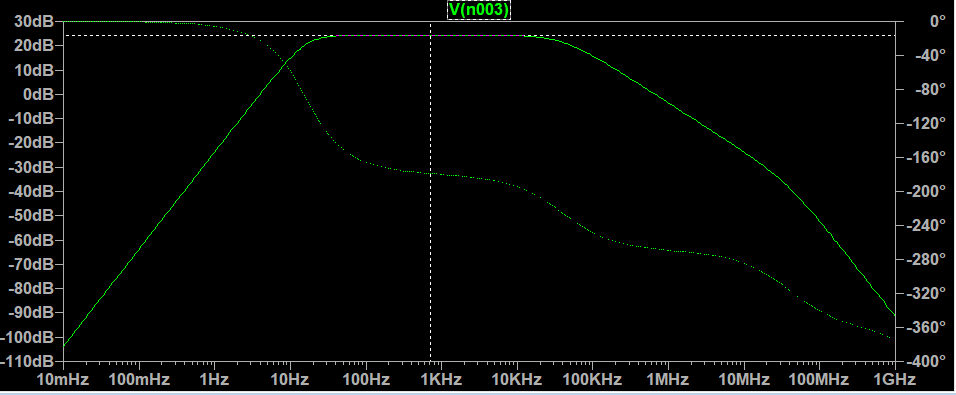
OUTPUT SIGNAL:

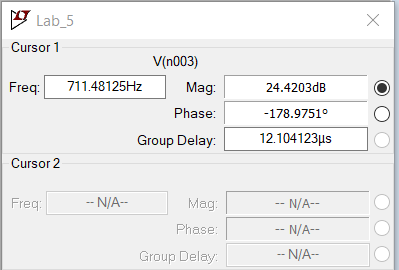




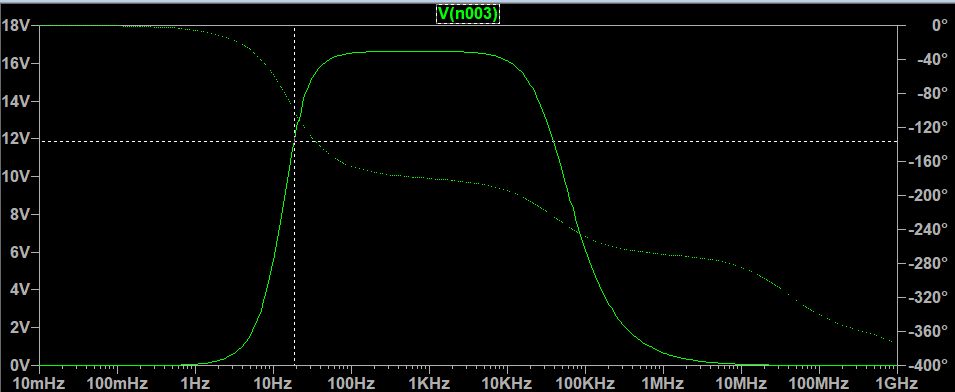
AC ANALYSIS:

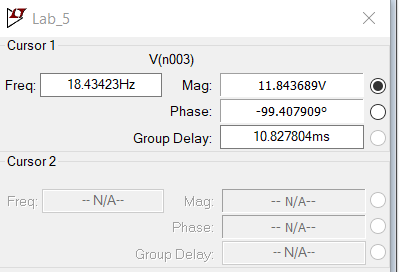
IN DB:

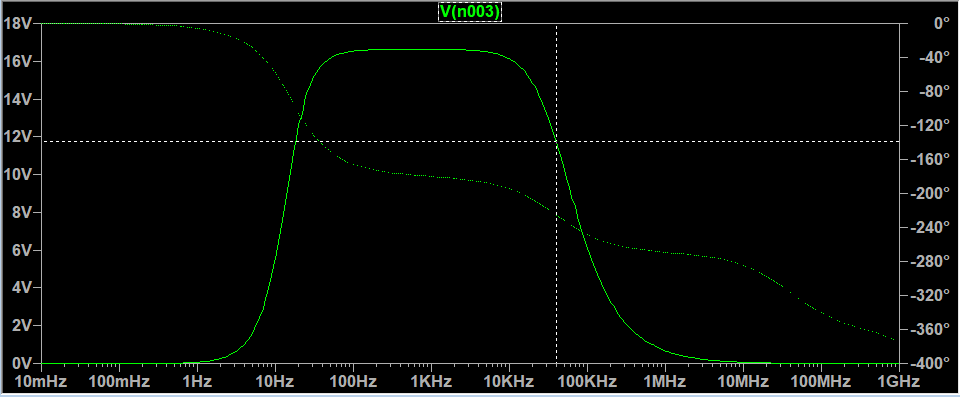


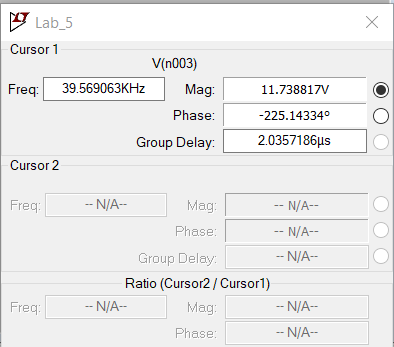


3-DB FORWARD AND BACKWARD: -

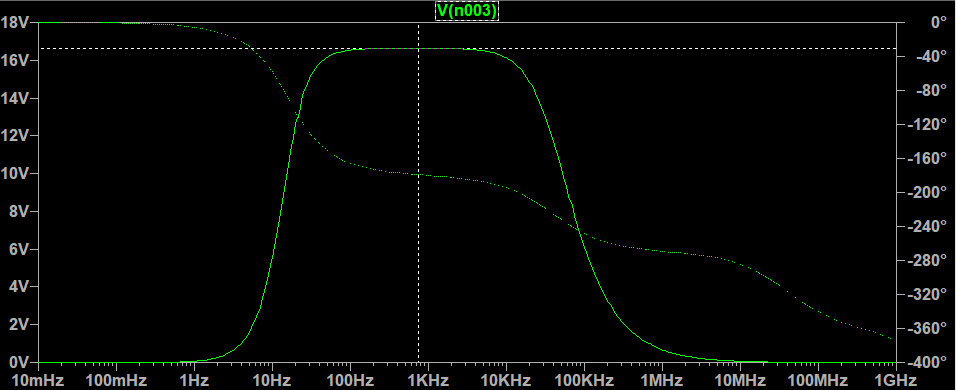


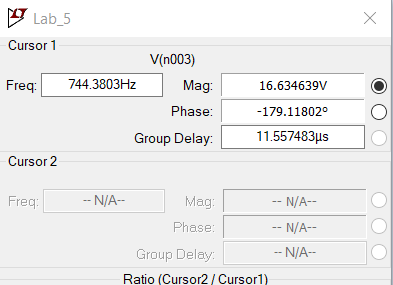






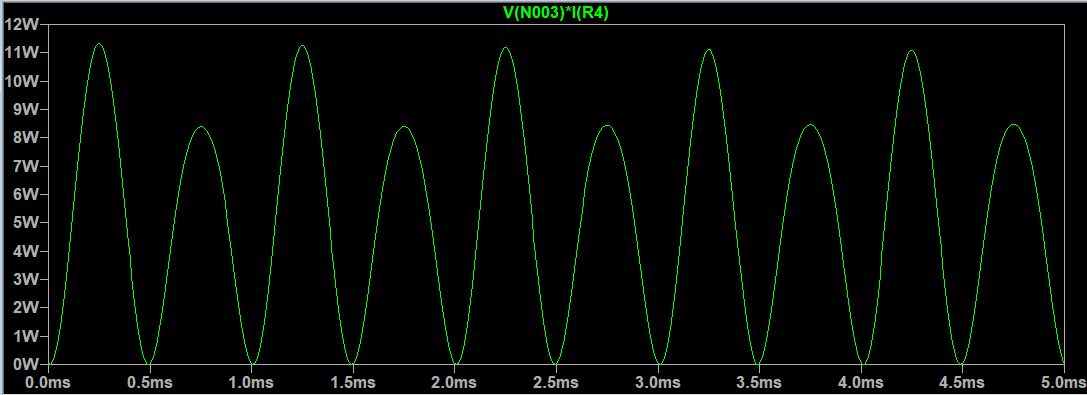
PLOT GAIN: -

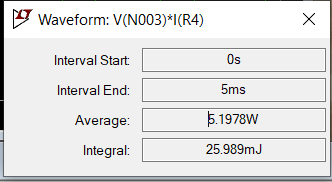




POWER ANALYSIS: -

OUTPUT:





DC ANALYSIS:

